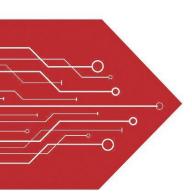
MSKSEMI















ESD

TVS

TSS

MOV

GDT

PLED

Broduct data speet

Semiconductor

Compiance

Features

Ultra low leakage: nA level Operating voltage: 5V Low clamping voltage

Complies with following standards:

– IEC 61000-4-2 (ESD) immunity test

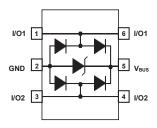
Air discharge: ±30kV
Contact discharge: ±30kV

- IEC61000-4-4 (EFT) 40A (5/50ns)

-IEC61000-4-5 (Lightning) 6A (8/20µs)

RoHS Compliant





Applications

USB 2.0 interface 10/100 Ethernet Personal digital assistants (PDA's) Notebooks, Desktops and Servers Portable instrumentation Digital cameras

Mechanical Characteristics

Package: SOT23-6 Lead

Finish: Matte Tin

UL Flammability Classification Rating 94V-0 Pb-Free, Halogen Free, RoHS/WEEE Compliant

Absolute Maximum Ratings (T_A=25°C unless otherwise specified)

Parameter	Symbol	Value	Unit	
Peak Pulse Power (8/20µs)	Ppk	150	W	
Peak Pulse Current (8/20μs)	Ipp	6	Α	
ESD per IEC 61000-4-2 (Air)	\/ E 0D	±30	kV	
ESD per IEC 61000-4-2 (Contact)	VESD	±30		
Operating Temperature Range	TJ	−55 to +125	°C	
Storage Temperature Range	Tstg	−55 to +150	°C	

Electrical Characteristics (T_A =25°C unless otherwise specified)

Parameter	Symbol	Test Condition	Min	Тур	Max	Unit
Reverse stand-off voltage	V_{RWM}				5	V
Reverse breakdown voltage	V_{BR}	I _{BR} =1mA	6		9	V
Reverse leakage current	I _R	V _R =5V			0.1	3 A
Clamping voltage (tp=8/20 ₃ s)	V _C	I _{PP} =1A			10	V
Clamping voltage (tp=8/20 ₃ s)	Vc	I _{PP} =6A			15	V
Junction capacitance	Cı	0Vdc,f=1MHz Between I/O pins and GND		0.8	1	pF
·	Cı	0Vdc,f=1MHz I/O to I/O		0.35	0.4	pF

Typical Characteristics

Fig1. $8/20\mu$ s Pulse Waveform

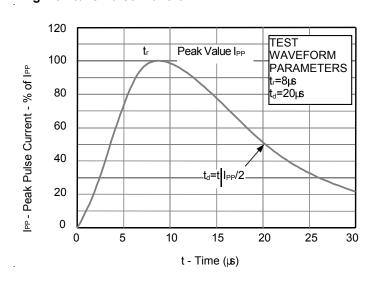
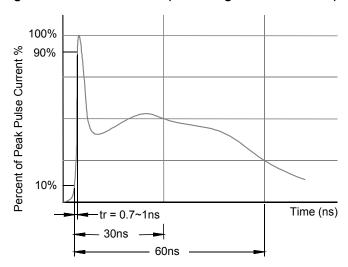
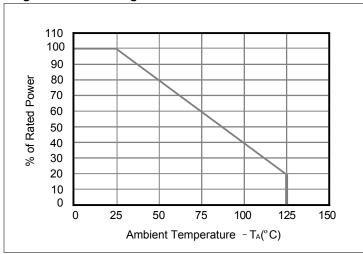


Fig2. ESD Pulse Waveform (according to IEC 61000-4-2)



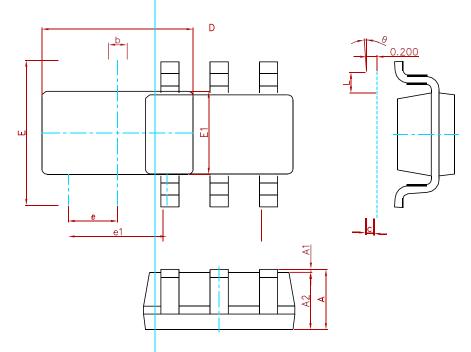




Semiconductor

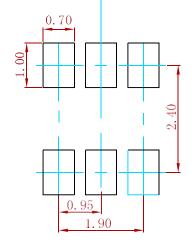
Compiance

PACKAGE MECHANICAL DATA



Symbol	Dimensions In Millimeters		Dimension	s In Inches
Syllibol	Min.	Max.	Min.	Max.
Α	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
С	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E1	1.500	1.700	0.059	0.067
Е	2.650	2.950	0.104	0.116
е	0.950(BSC)	0.037	(BSC)
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°

Suggested Pad Layout



Note:

- 1. Controlling dimension:in millimeters.
 2. General tolerance:± 0.05mm.
 3. The pad layout is for reference purposes only.

REEL SPECIFICATION

P/N	PKG	QTY
MSUSBLC6-2SC6	SOT-23-6	3000



Semiconductor

Compiance

Attention

- Any and all MSKSEMI Semiconductor products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your MSKSEMI Semiconductor representative nearest you before using any MSKSEMI Semiconductor products described or contained herein in such applications.
- MSKSEMI Semiconductor assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all MSKSEMI Semiconductor products described or contained herein.
- Specifications of any and all MSKSEMI Semiconductor products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.
- MSKSEMI Semiconductor. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with someprobability. It is possiblethat these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits anderror prevention circuitsfor safedesign, redundant design, and structural design.
- In the event that any or all MSKSEMI Semiconductor products(including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from theauthorities concerned in accordance with the above law.
- No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of MSKSEMI Semiconductor.
- Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production. MSKSEMI Semiconductor believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.
- Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. Whendesigning equipment, referto the "Delivery Specification" for the MSKSEMI Semiconductor productthat you intend to use.